



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Daniel L. Auclair et al.  
Title: Soft Errors Handling in EEPROM Devices  
Application No.: 08/908,265 Filing Date: August 7, 1997  
Examiner: Moise, Emmanuel Lionel Group Art Unit: 2133  
Docket No.: SNDK.026US3 (formerly Conf. No.: 9300  
M-10202-3C US)

RECEIVED  
MAY 30 2003

Certificate of Mailing Under 37 CFR 1.8

Technology Center 2100

I hereby certify that this correspondence is being deposited with the United States  
Postal Service with sufficient postage as first class mail in an envelope addressed to:  
Commissioner for Patents, Washington, D.C. 20231, on 5/27/03

Julieen Bown  
Signature

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Dear Sir:

The applicants respond to the Official Action mailed on May 5, 2003, as follows:

In the Claims:

Please amend claim 36 as indicated:

D  
36.(Amended) The memory device of claim 35, wherein a group of the plurality of memory cells are arranged in a row that includes the first cell, the memory device further comprising means for rewriting a previously stored value into each of the group of memory cells when the means for determining determines that the first cell has a degraded state.

Please amend claims 45-47 as indicated:

SC  
25  
6/4/03